

International Forum on Recent Trends in Power Electronics



# Latest Findings in Three-Phase



Swiss Federal Institute of Technology (ETH) Zurich Power Electronic Systems Laboratory www.pes.ee.ethz.ch

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## Latest Findings in Three-Phase AC/DC Converter Research



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## Outline

- ► Introduction
- High-Power EV Battery Charging
- Advanced Drive Systems
- **Conclusions**











## **ETH Zurich**

21	Nobel Prizes
509	Professors
5800	T&R Staff
2	Campuses
136	Labs
35%	Int. Students
90	Nationalities
36	Languages

150<sup>th</sup> Anniv. in 2005



#### **Departments**

ARCH **Architecture** BAUG **Civil, Environmental and Geomatics Eng.** BIOL **Biology** BSSE **Biosystems Chemistry and Applied Biosciences CHAB Earth Sciences** ERDW Humanities, Social and Political Sciences GESS HEST Health Sciences, Technology **Computer Science** INFK Information Technology and Electrical Eng. ITET **Mathematics** MATH **Materials Science** MATL **Mechanical and Process Engineering** MAVT Management, Technology and Economy MTEC PHYS **Physics** USYS **Environmental Systems Sciences** 

#### Students ETH in total

14′500	B.Sc.+M.ScStudents		
4′000	<b>Doctoral Students</b>		



## **ITET** - Power Electronic Systems Laboratory









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#### **Power Electronic Systems Laboratory**



23 Ph.D. Students 2 Sen. Level Researchers

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- **China** EV Charging Equipment Supplier Qualification Standard
- Extremely Wide DC Output Voltage Range



► Buck-Boost Functionality → Boost-Type PFC Rectifier & Back-End Buck Converter





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**Buck-Stage Utilized for DC Link Voltage Shaping / Switching of Single Mains Phase** 





#### 1-out-of-3 *Boost+Buck* AC/DC Converter

- Single Phase PWM Operation → Low Switching Losses / High Efficiency
- Cont. Input & Output Currents



► High Output Voltage → Operation as *Conv. Boost-Type* PWM Rectifier / Clamped Buck-Stage





- Individual DC Link Voltages of the Phases
- AC Input Phase Voltages Generated with Reference to DC-Minus
- DC Link Voltages Adapted to Required AC Input Phase Voltage



- Continuous Input and Output Currents
- ► Clamping of Boost or Buck Bridge Leg of Phase Module → Low Switching Losses





- Individual DC Link Voltages of the Phases
- AC Input Phase Voltages Generated with Reference to DC-Minus
- DC Link Voltages Adapted to Required AC Input Phase Voltage



- Continuous Input and Output Currents





- *"Buck-Boost" Instead of "Boost-Buck" Phase Modules* AC Input Phase Voltages Generated with Reference to DC-Minus



- No Intermediate DC Link Voltages
  - Converter Integrated Filter Inductors  $\rightarrow$  High Power Density
- Clamping of Boost or Buck Bridge Leg  $\rightarrow$  Low Switching Losses





Input Current & Output Voltage Control



Cascaded Single Energy Storage Control Loops
 Seamless Transition between Boost- & Buck-Mode 
 *"Democratic" Control*







Isolated Single-Stage AC/DC Converter





#### **Dual 3-** $\Phi$ Active Bridge Converter

- HF-Components of Boost Ind. Voltages Utilized for Power Transfer
   Dual Active Bridge Concept
- ZVS



Three-Port System - AC Input / Isol. DC Output / Non-Isol. DC Output





#### Dual 3- $\Phi$ Active Bridge Converter

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Three-Port System - AC Input / Isol. DC Output / Non-Isol. DC Output





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#### **Dual 3-** $\Phi$ **Active Bridge Converter**

■ Multi-Objective Optimization → *Efficiency / Power Density Pareto Front* 

EMI filter

(5 W)

- Volume and Loss Distribution
- $P = 8 kW, 400 V_{AC} / 400 V_{DC}$



Efficiency > 98% in Wide Output Power Range @ 4kW/dm<sup>3</sup> Power Density (65W/in<sup>3</sup>)

![](_page_19_Picture_7.jpeg)

![](_page_19_Picture_8.jpeg)

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gate drivers, control, fan

![](_page_20_Picture_0.jpeg)

![](_page_20_Picture_1.jpeg)

![](_page_20_Picture_2.jpeg)

![](_page_20_Picture_3.jpeg)

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### **Inverter / Drive Applications**

- Battery or Fuel-Cell Supply → Wide DC Input Voltage Range Matching of Supply & Rated Motor Voltage

![](_page_21_Figure_4.jpeg)

Inverter Input Voltage Adaption by DC/DC Boost Converter

![](_page_21_Picture_6.jpeg)

#### **Inverter / Drive Applications**

- Front-End DC/DC Boost Converter DC Link Voltage Adaption
   SiC Power Semiconductors → Low Switching (& Conduction) Losses

![](_page_22_Figure_4.jpeg)

Motor Winding Insulation Stress / Reflections on Long Motor Cables / Bearing Currents

![](_page_22_Picture_6.jpeg)

![](_page_22_Picture_7.jpeg)

### **Output Filter Requirement (1)**

Ultra-Fast Switching of WBG Power MOSFETs

LDI

LLo

LSI

L<sub>D2</sub>

LS2

D

TO-247-4

TO-247-3

Typical dv/dt of 30...50kV/us

01

![](_page_23_Figure_4.jpeg)

Motor Winding Insulation Stress / Reflections on Long Motor Cables / Bearing Currents

![](_page_23_Picture_6.jpeg)

Vdc

![](_page_23_Picture_7.jpeg)

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#### **Output Filter Requirement (2)**

- Long Motor Cables  $\rightarrow 2x U_{DC}$  Overvoltage / Insul. Stress
- Application Restrictions (NEMA Standard)

![](_page_24_Figure_4.jpeg)

	Motor Cable Length <sup>1</sup>					
Power Line Voltage	up to 75 ft	up to 100 ft	up to the max. length in the "Motor Connection Spec."	longer		
208 – 240 V AC	Gen (NE	(3)				
480 V AC	General Purpose Motor (NEMA MG 1, Part 30)		Inverter Duty Motor (NEMA MG 1, Part 31) <sup>2</sup>	(3)		
575 – 600 V AC	General Purpose Motor I (NEMA MG 1, Part 30) (N		nverter Duty Motor EMA MG 1, Part 31) <sup>2</sup>	(3)		

![](_page_24_Figure_6.jpeg)

0 V

 $\mathcal{N}$ 

- 1. These maximum motor cable lengths are rules of thumb based on the motor's stator insulation and the VFD's maximum motor cable length capability. RFI/EMC concerns are not taken into account. All lengths are based on the ACH550 VFD. Competitive VFDs may need a significantly shorter motor cable length.
- 2. Follow the maximum cable length recommendations of the motor manufacturer, if they are more restrictive.
- 3. For motor cable lengths longer than the VFD's recommendation, a sine wave filter and/or other considerations may be required. Contact ABB.

Imped. Matching @ Motor Terminals / Double Transition PWM / dv/dt- or Sine Wave Filters

![](_page_24_Picture_11.jpeg)

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Cable = 3m

#### **Output Filter Requirement (3)**

- CM Inverter Output Voltage  $\rightarrow$  Shaft Voltage  $\rightarrow$  Electrical Discharge in Bearing ("EDM")
- CM Conducted  $EMI \rightarrow Expensive$  Shielded Motor Cables

![](_page_25_Figure_4.jpeg)

► Cond. Grease / Ceram. Bearings / Shaft Grndg Brushes / dv/dt- or Sine Wave Filters

![](_page_25_Picture_6.jpeg)

![](_page_25_Picture_7.jpeg)

#### **Buck-Boost Inverters with Output Filter**

- Boost Converter & *Voltage DC Link Inverter* with LC Output Filter
- Buck Converter & Current DC Link Inverter ("Integrated Filter")

![](_page_26_Figure_4.jpeg)

► Large Number of Ind. Components OR Large Number of Power Semiconductors

![](_page_26_Picture_6.jpeg)

![](_page_26_Picture_7.jpeg)

#### **Buck-Boost Inverter with Output Filter**

- Battery or Fuel-Cell Supply → Wide DC Input Voltage Range
- Matching of Supply & Rated Motor Voltage

![](_page_27_Figure_4.jpeg)

Motor Winding Insulation Stress / Reflections on Long Motor Cables / Bearing Currents

![](_page_27_Picture_6.jpeg)

![](_page_27_Picture_7.jpeg)

![](_page_28_Picture_1.jpeg)

- Three-Phase Continuous Output / Low EMI !
- Buck+Boost Operation / Wide Input &/or Output Range Industrial Drive
   Standard Bridge Legs / Building Blocks 1.2kV SiC MOSFE
   ZVS Operation / Extreme Power Density

![](_page_28_Figure_6.jpeg)

- 1.2kV SiC MOSFETs

![](_page_28_Figure_8.jpeg)

**Project Scope**  $\rightarrow$  Hardware Demonstrator / Exp. Analysis / Comparative Evaluation

![](_page_28_Picture_10.jpeg)

![](_page_28_Picture_11.jpeg)

• Operating Behavior

![](_page_29_Figure_3.jpeg)

![](_page_29_Picture_4.jpeg)

![](_page_29_Picture_5.jpeg)

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#### **Y-Inverter**

- Modulation Schemes
- Output Voltage DC Offset for Low Modulation Index
- Third Harmonic Injection OR Phase Clamping

![](_page_30_Figure_5.jpeg)

Reduced Output Voltage Amplitude / Reduction of Sw. Losses

![](_page_30_Picture_7.jpeg)

![](_page_31_Figure_0.jpeg)

■ *"Democratic Control"* → Seamless Transition Between Buck & Boost Operation

![](_page_31_Picture_2.jpeg)

![](_page_31_Picture_3.jpeg)

- Demonstrator Specifications
- Wide Input Voltage Range  $\rightarrow$  400...750V<sub>DC</sub>
- Max. Input Current  $\rightarrow \pm 15A$

![](_page_32_Figure_5.jpeg)

![](_page_32_Figure_6.jpeg)

Max. Output Power

- $\rightarrow$  6...11 kW
- Output Frequency Range
   Output Voltage Ripple
- → 0...500Hz
- → 3.2V Peak-to-Peak (incl. Add. Output Filter)

![](_page_32_Picture_13.jpeg)

![](_page_32_Picture_14.jpeg)

- System Design
- Identification of Worst Case Component Stresses
- Analysis in Input Voltage / Output Voltage / Output Power 3D-Design Space

![](_page_33_Figure_5.jpeg)

**Example of Inductor Current Stress Analysis**  $\rightarrow$   $I_{L,rms}$  and  $I_{L,pk}$ 

![](_page_33_Picture_7.jpeg)

![](_page_33_Picture_8.jpeg)

#### • Demonstrator Power Circuit

![](_page_34_Figure_3.jpeg)

- Inductors → 2 x EELP 43 Ferrite Cores / N97 per Phase
   Add. Output Filter → 3.2V Peak-to-Peak Output Voltage Ripple
   Power Semiconductors → 3 x Cree 1200V/75mΩ SiC MOSFETs per Switch Mounted on IMS

![](_page_34_Picture_8.jpeg)

![](_page_34_Picture_9.jpeg)

• Demonstrator Power Circuit

![](_page_35_Figure_3.jpeg)

- Inductors → 2 x EELP 43 Ferrite Cores / N97 per Phase
   Add. Output Filter → 3.2V Peak-to-Peak Output Voltage Ripple
   Power Semiconductors → 3 x Cree 1200V/75mΩ SiC MOSFETs per Switch Mounted on IMS

![](_page_35_Picture_8.jpeg)

![](_page_35_Picture_9.jpeg)

• Demonstrator Performance - Volume Distribution

![](_page_36_Figure_3.jpeg)

**Power Density**  $\rightarrow$  15kW/dm<sup>3</sup> (0.73dm<sup>3</sup>)

![](_page_36_Picture_5.jpeg)

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• Demonstrator Performance - Loss Distribution (Design Margin Considered)

![](_page_37_Figure_3.jpeg)

98% Efficiency Target @ Rated Power & Input/Output Voltage

![](_page_37_Picture_5.jpeg)

![](_page_37_Picture_6.jpeg)

• Demonstrator Performance - Efficiency over Output Power @ Given Input Voltage

![](_page_38_Figure_3.jpeg)

Higher Efficiency for *Phase Clamping Modulation*

![](_page_38_Picture_5.jpeg)

![](_page_38_Picture_6.jpeg)

#### • Demonstrator - Virtual Prototype (1)

![](_page_39_Figure_3.jpeg)

![](_page_39_Picture_4.jpeg)

![](_page_39_Picture_5.jpeg)

• Demonstrator - Virtual Prototype (2)

![](_page_40_Picture_3.jpeg)

**Dimensions**  $\rightarrow$  160 x 110 x 42 mm<sup>3</sup>

![](_page_40_Picture_5.jpeg)

![](_page_40_Picture_6.jpeg)

• Alternative Power Circuit Topology

![](_page_41_Figure_3.jpeg)

■ Lower Number of Switches / Higher Component Stresses → Low Power Applications

![](_page_41_Picture_5.jpeg)

![](_page_41_Picture_6.jpeg)

## **Conclusions**

**Future Need for "SWISS Knife"-Type Power Converters** 

- \* Wide Input / Load Voltage Range
  \* Standard Building Blocks / Modular
  \* Bidirectional Power Transfer
  \* Electromagnetically "Quiet"
  \* 10kW/dm<sup>3</sup> Power Density incl. EMI Filter @ Air Cooling
- \* 98% Efficiency

► Y-Inverter / Rectifier

- \* Next Generation Integrated Motor Drives
- \* Next Generation PMW Rectifiers

![](_page_42_Picture_12.jpeg)

![](_page_42_Picture_14.jpeg)

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### Thank You !

![](_page_43_Picture_2.jpeg)

![](_page_43_Picture_3.jpeg)

![](_page_43_Picture_4.jpeg)