

Monolithic Bi-Directional Switches — X-Technology of 3- Φ AC/DC Mains Interfaces

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Part #3
Special Session

New Developments in
WBG Bidirectional Switches
and Applications

Oct. 11, 2022



M-BDS — X-Technology of 3- Φ AC/DC Mains Interfaces

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Outline



- ▶ *Introduction*
- ▶ *Diode-Bridge & Active Filter*
- ▶ *Boost-Type / VIENNA Rectifier*
- ▶ *Buck-Boost Type Converters*
- ▶ *Conclusions*

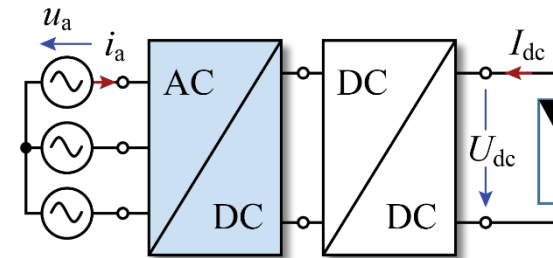
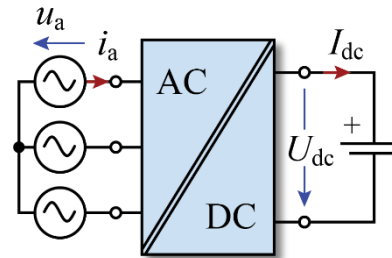
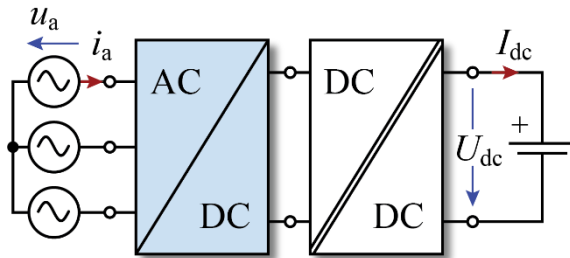
Acknowledgement

F. Krismer

3- Φ AC/DC Converter Application Areas

- *Electric Vehicle Battery Charging*
- *Datacenter Power Supply*
- *Renewable Energy Applications*

Typ. $200 \dots 1000 V_{DC}$ EV Battery
Voltage Range



$320 \dots 530 V_{rms}$
Line-to-Line

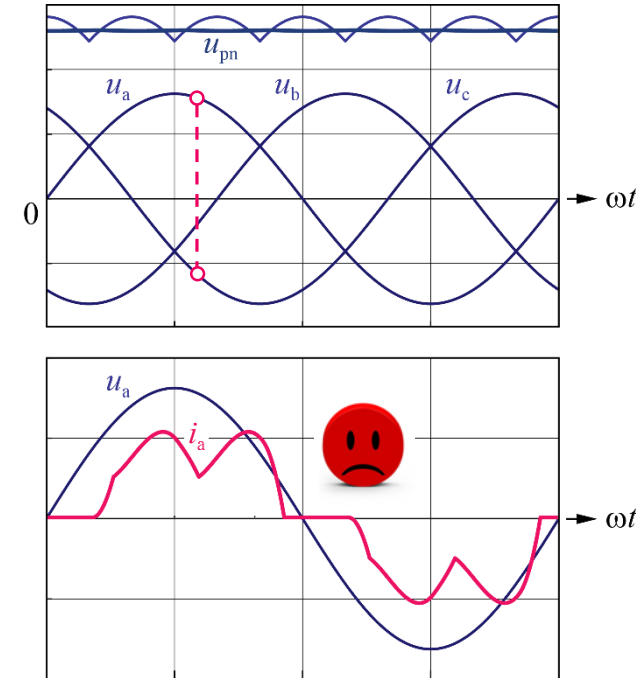
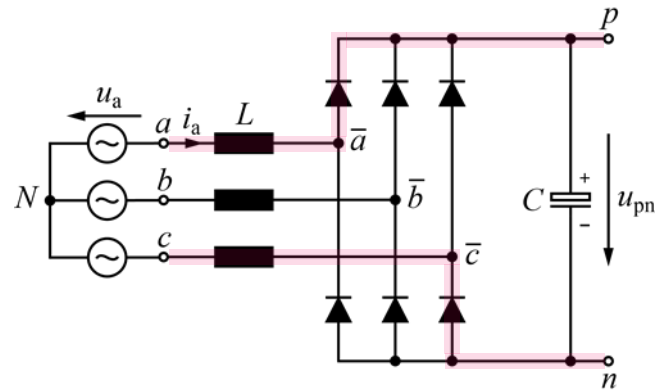
MPP Tracking in $60 \dots 90\%$ of
Max. Open Circuit Voltage

- *Non-Isolated* OR *Isolated Output*
- *Wide AC Input* &/OR *DC Output Voltage Range*
- *Unidirectional* OR *Bidirectional Power Transfer*

—— *3rd Harmonic Inj. Diode-Bridge Rectifier* ——
3-Level Boost-Type VIENNA Rectifier

3- Φ Diode Bridge Rectifier

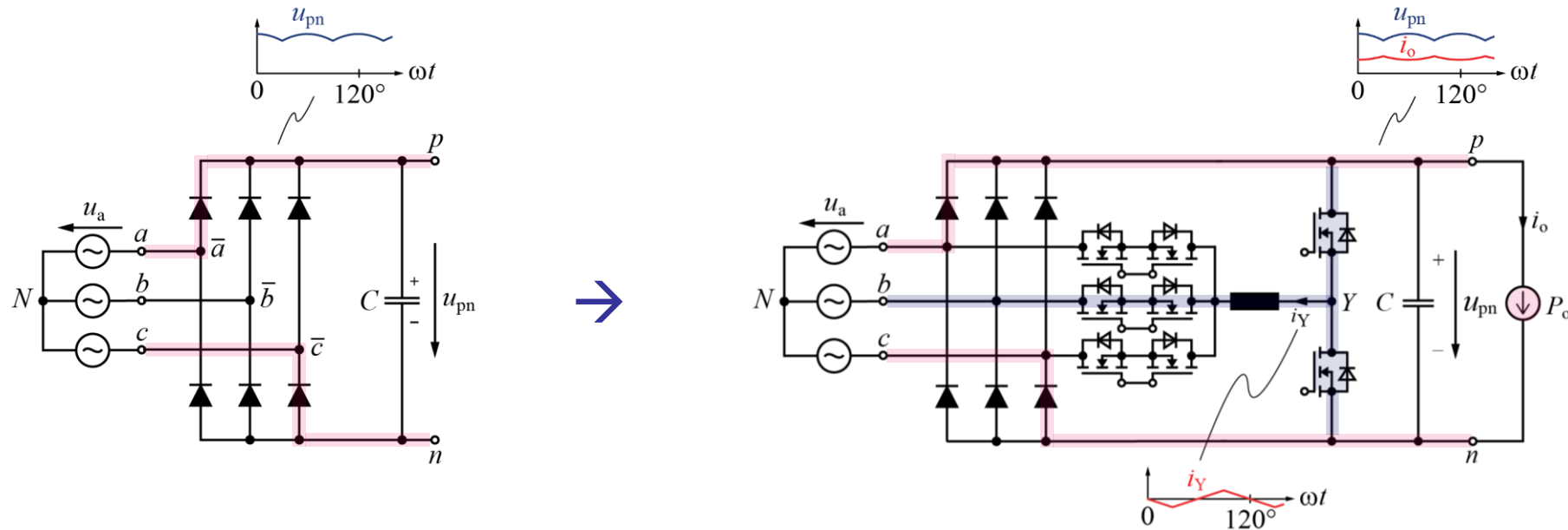
- Conduction States Defined by Line-to-Line Mains Voltages
- Intervals with Zero Current / LF Harmonics
- No Output Voltage Control



→ Active Mains Current Shaping / Simultaneous Current Flow in All Phases

Integrated Active Filter (IAF) PFC Rectifier

- **3rd Harmonic Current Injection** into Phase with Lowest Voltage
- **Phase Selector AC Switches** Operated @ Mains Frequency — “3- Φ Unfolder”

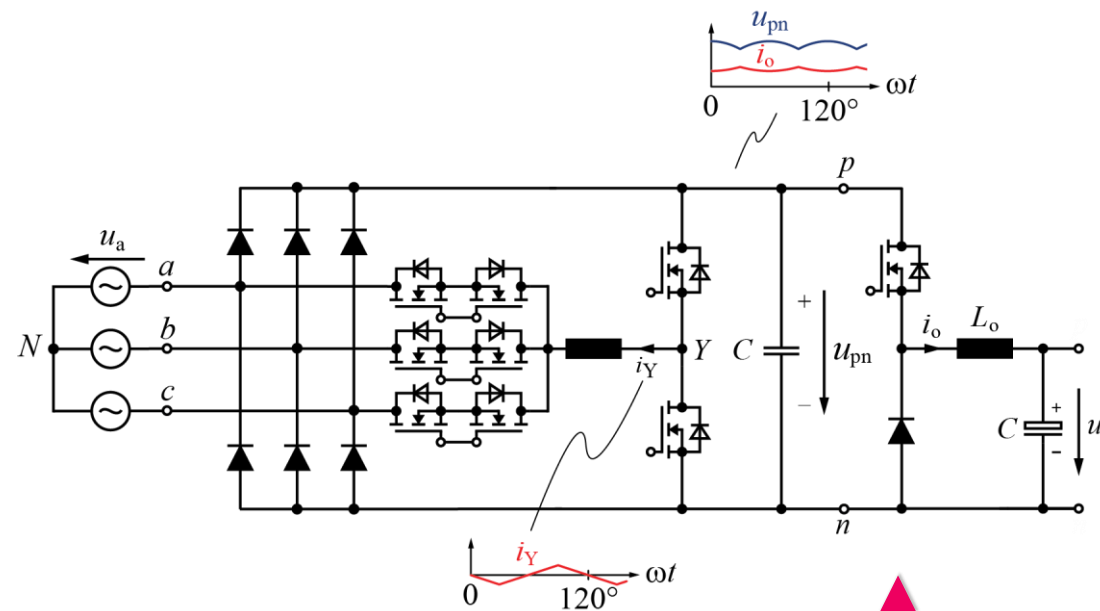


- **Non-Sinusoidal Mains Current**

- $P_o = \text{const. Required}$
- **Sinusoidal Mains Current**
- **NO (!) DC Voltage Control**

IAF Rectifier & Buck Output Stage

- *Buck-Output Stage* → $P_o = \text{const.}$ & *Output Voltage Control*
- *Sinusoidal Mains Current*



- *Buck-Stage Could be Replaced by Isolated DC/DC Conv. or Inverter*

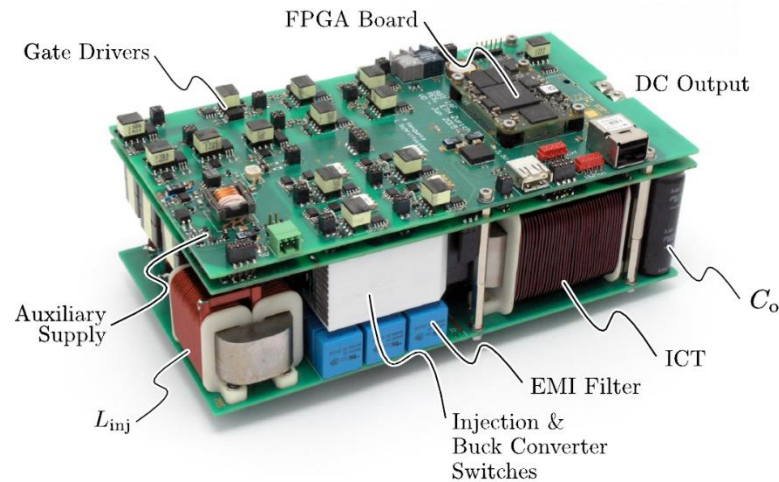
IAF Rectifier & Buck Output Stage

- **Efficiency $\eta > 99.1\%$ @ 60% Rated Load**
- **Mains Current $THD_I \approx 2\%$ @ Rated Load**
- **Power Density $\rho \approx 4\text{kW}/\text{dm}^3$**

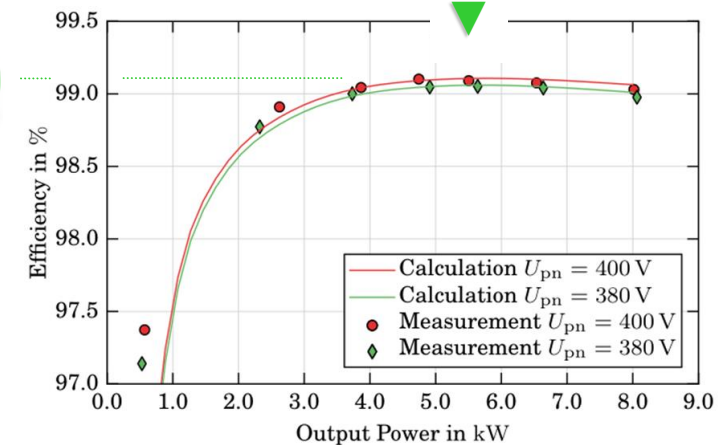
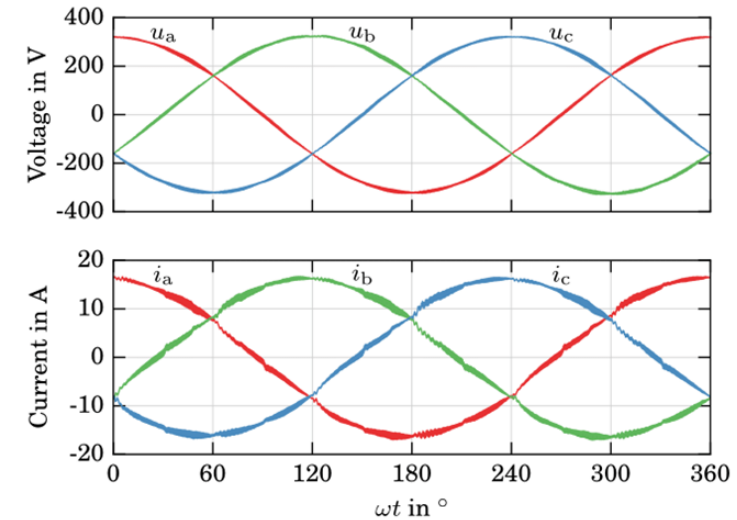
$$P_o = 8 \text{ kW}$$

$$U_N = 400\text{V}_{AC} \rightarrow U_o = 400\text{V}_{DC} \text{ const. / Controlled}$$

$$f_s = 27\text{kHz}$$



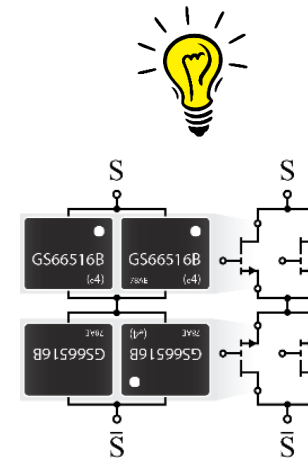
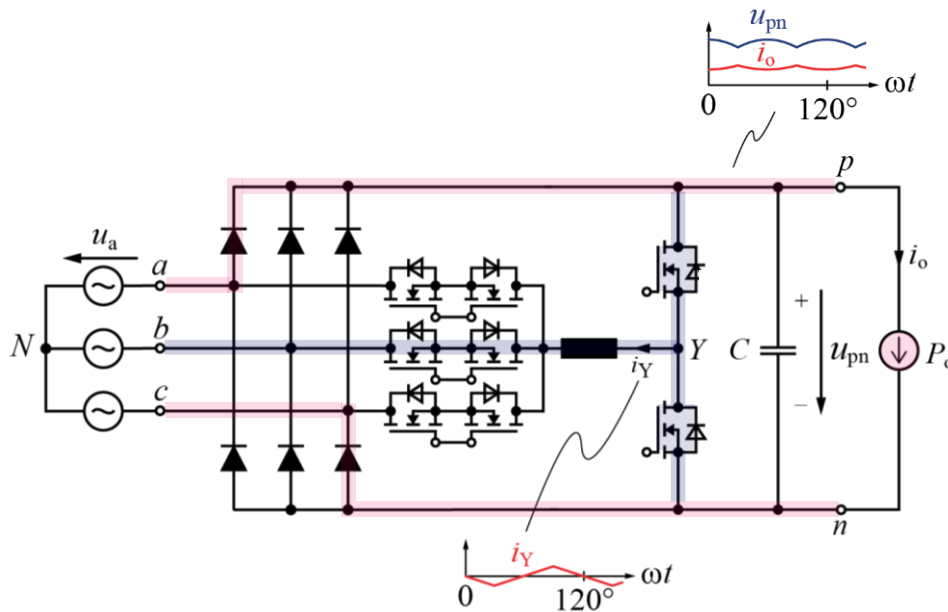
- **SiC Power MOSFETs & Diodes**
- **2 Interleaved Buck DC/DC Output Stages (!)**



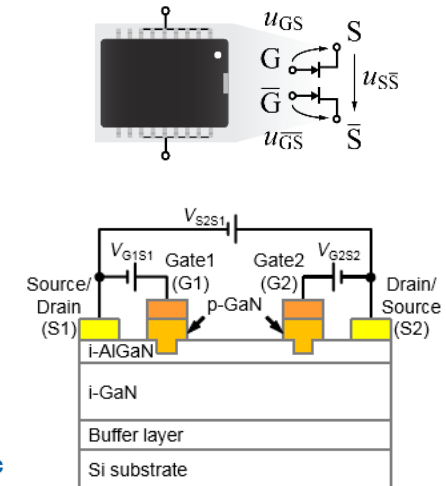
Remark Application of M-BDS

- **M-BDS** — *Monolithic Bidirectional / Bipolar Switch*
- Realization of the *Phase Selector Switches* of 3rd Harmonic Inj. PFC Rectifiers
- Bipolar Voltage Blocking / Current Carrying Capability

NEW
ARRIVAL



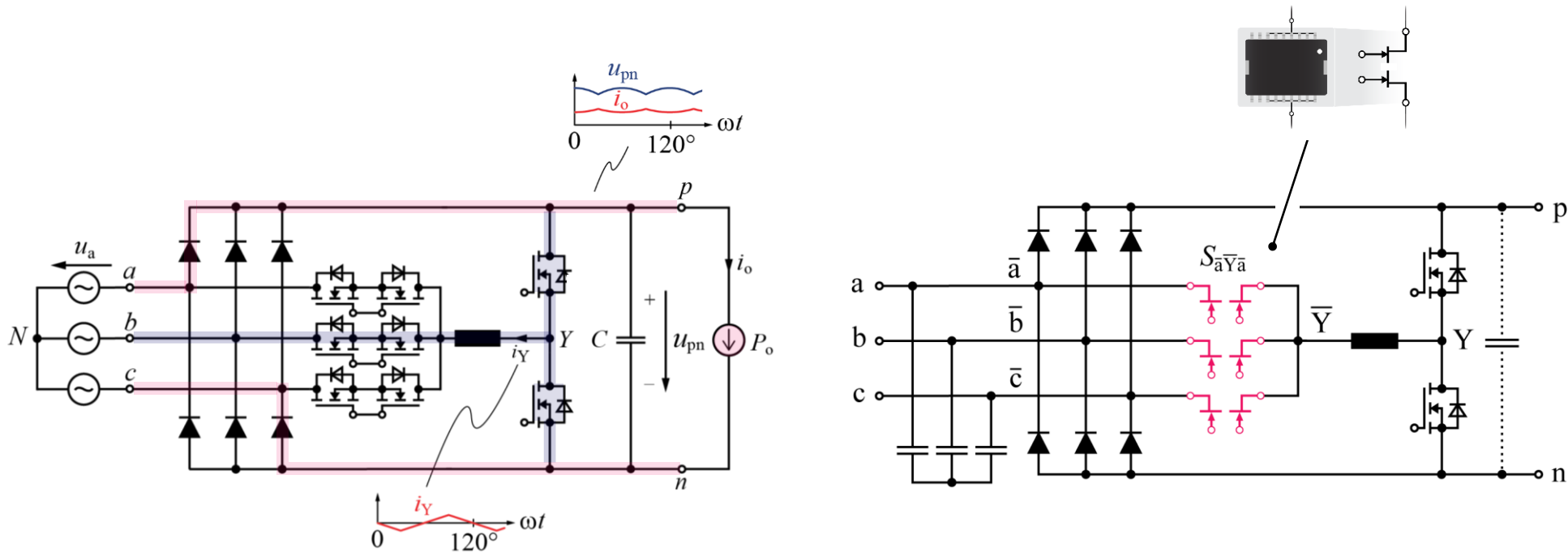
Source: **Panasonic**
ideas for life



- **M-BDS** → *Factor of 4 Reduction of Chip Area Comp. to Discrete Realization w/ Same R_{on} (!)*

Remark Application of M-BDS

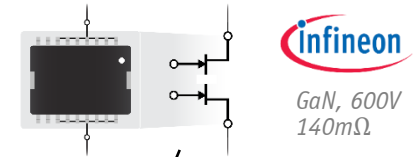
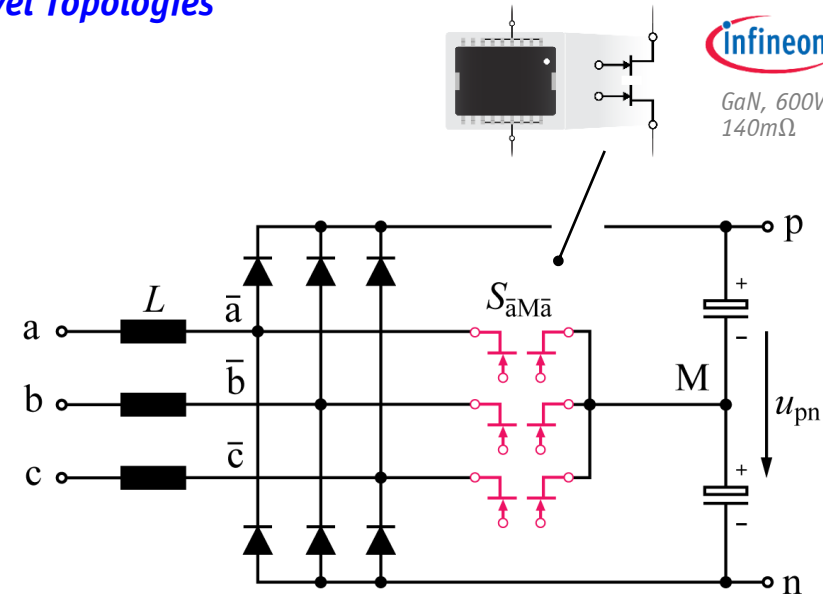
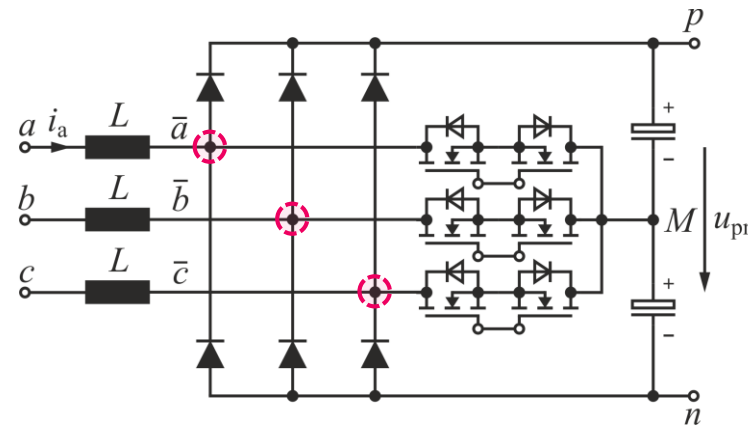
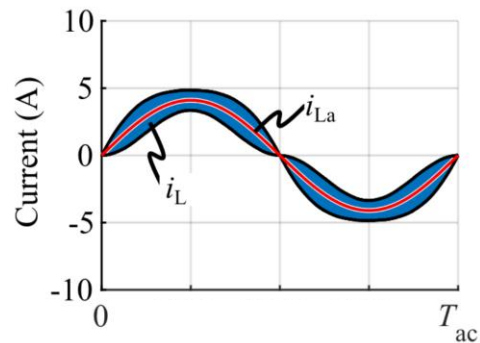
- Realization of the **Phase Selector Switches** of 3rd Harmonic Inj. PFC Rectifiers
- Bipolar Voltage Blocking / Current Carrying Capability



- M-BDS → Factor of 4 Reduction of Chip Area Comp. to Discrete Realization w/ Same $R_{(on)}$ (!)

Remark 3-Level T-Type PFC (Vienna) Rectifier

- Control of Diode Bridge Input Voltage → Sinusoidal Input Current Shaping
- Controlled Output Voltage > Mains Line-to-Line Voltage Amplitude → Boost-Type
- Low Conduction Losses Compared to Alternative 3-Level Topologies

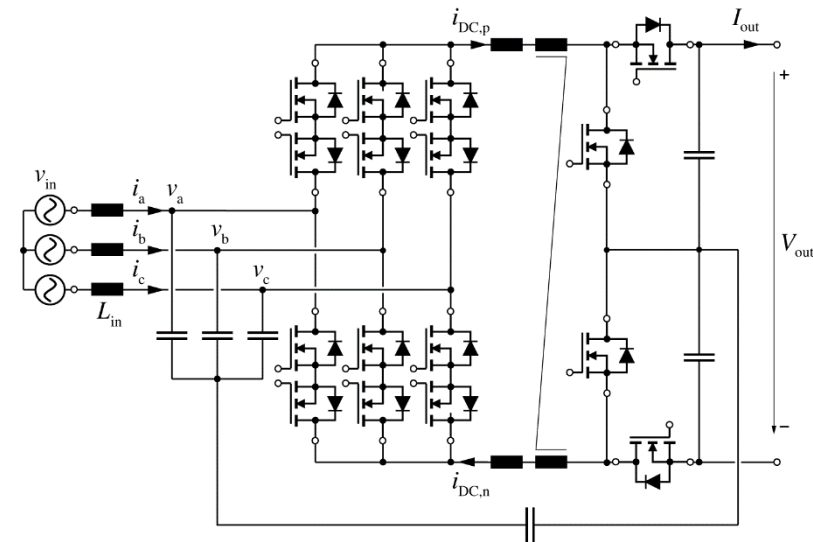
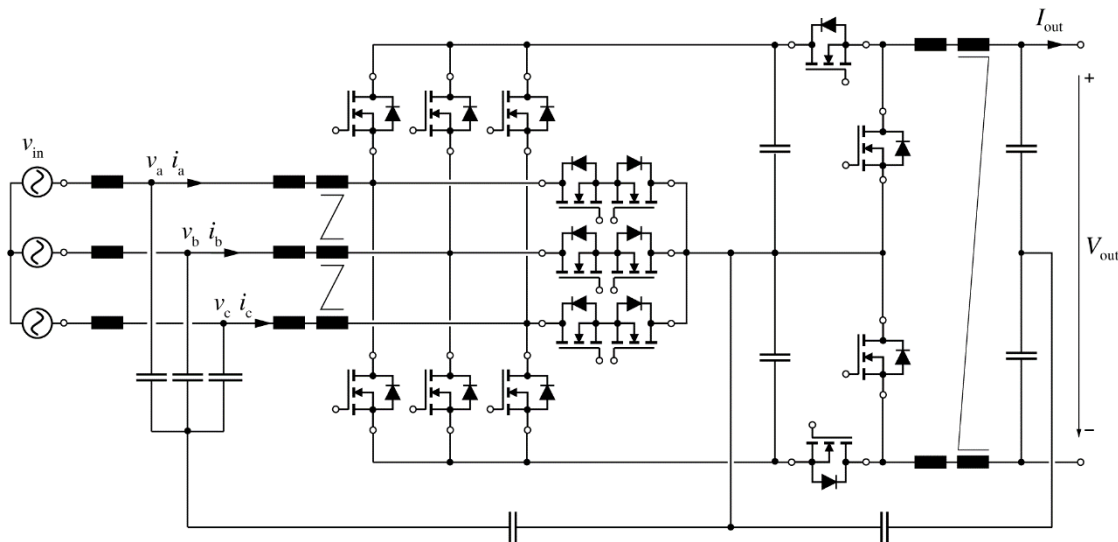


- Voltage Stress of T-Type Switches Defined by Half (!) the DC-Link Voltage
- Utilization of 600V GaN M-BDSs @ $U_{pn} = 800V_{DC}$ in Combination w/ 1200V SiC Diodes

*Buck-Boost
Voltage | Current DC-Link
PFC Rectifier*

Bidirectional *Buck-Boost* PFC Rectifier Concepts

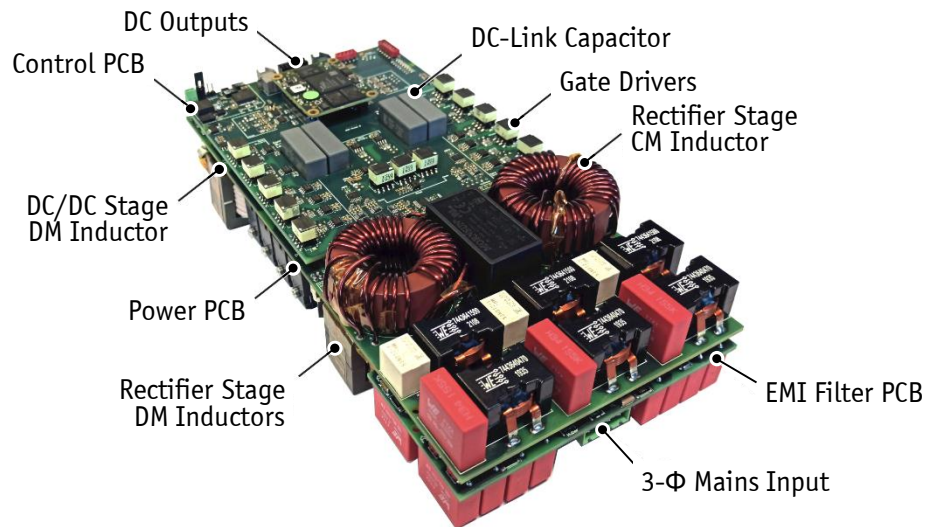
- *Boost—Buck OR Buck—Boost Combination*
- *Closed Loop vs. Open Loop Mains Current Control & Active Input Filter Damping*
- *“Synergetic Control” of AC/DC and DC/DC Converter Stage*



- *AC/DC Buck-Stage Output Inductor Utilized as DC/DC Boost Inductor → Min. # of Inductive Components*

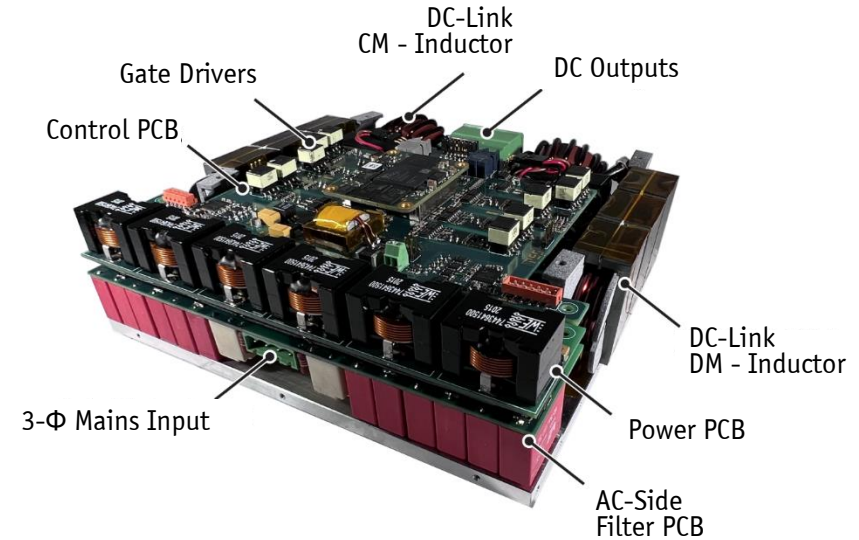
Boost-Buck | Buck-Boost Demonstrator Systems

- 10 kW @ 400...800 V_{DC} @ 3- Φ 400 V_{rms} Mains
- $U_{out} = 200 \dots 800 \text{ V}_{DC}$
- $\eta = 98.8\%$ @ 5.4 kW/dm³
- AC/DC — $f_{sw} = 100 \text{ kHz}$
- DC/DC — $f_{sw} = 2 \times 100 \text{ kHz} / 200 \text{ kHz eff.}$



- **Boost-Buck Voltage DC-Link PFC Rectifier**

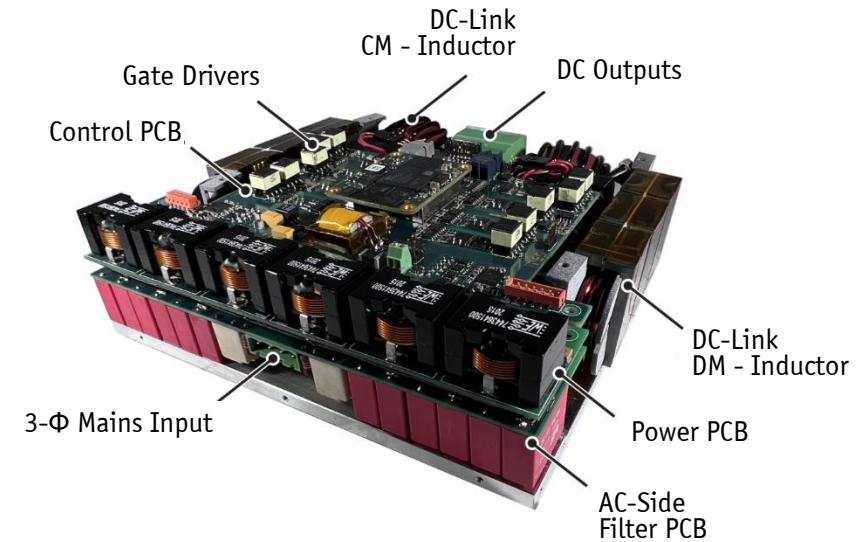
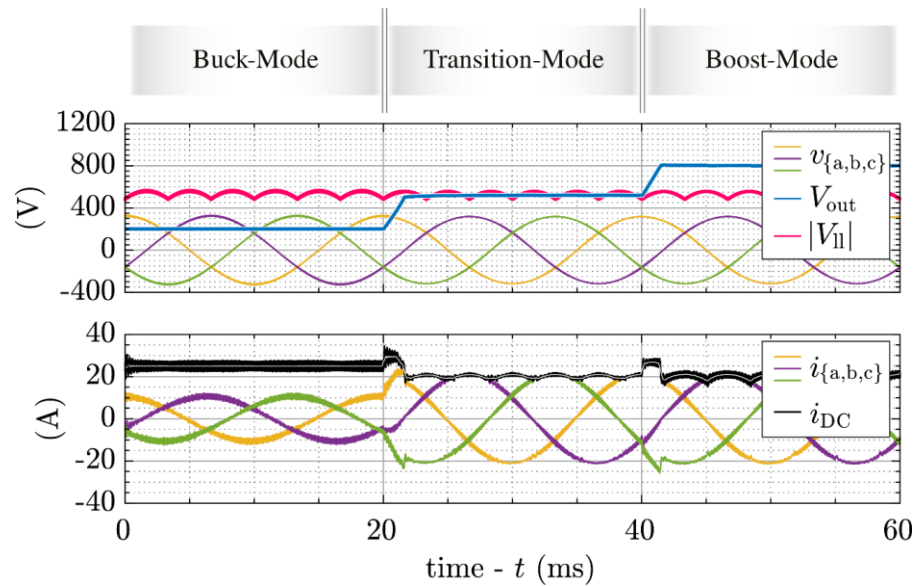
- 10 kW @ 400...1000 V_{DC} @ 3- Φ 400 V_{rms} Mains
- $U_{out} = 200 \dots 1000 \text{ V}_{DC}$
- $\eta = 98.6\%$ @ 6.4 kW/dm³
- AC/DC — $f_{sw} = 100 \text{ kHz}$
- DC/DC — $f_{sw} = 2 \times 50 \text{ kHz} / 100 \text{ kHz eff.}$



- **Buck-Boost Current DC-Link PFC Rectifier**

Buck-Boost Demonstrator System

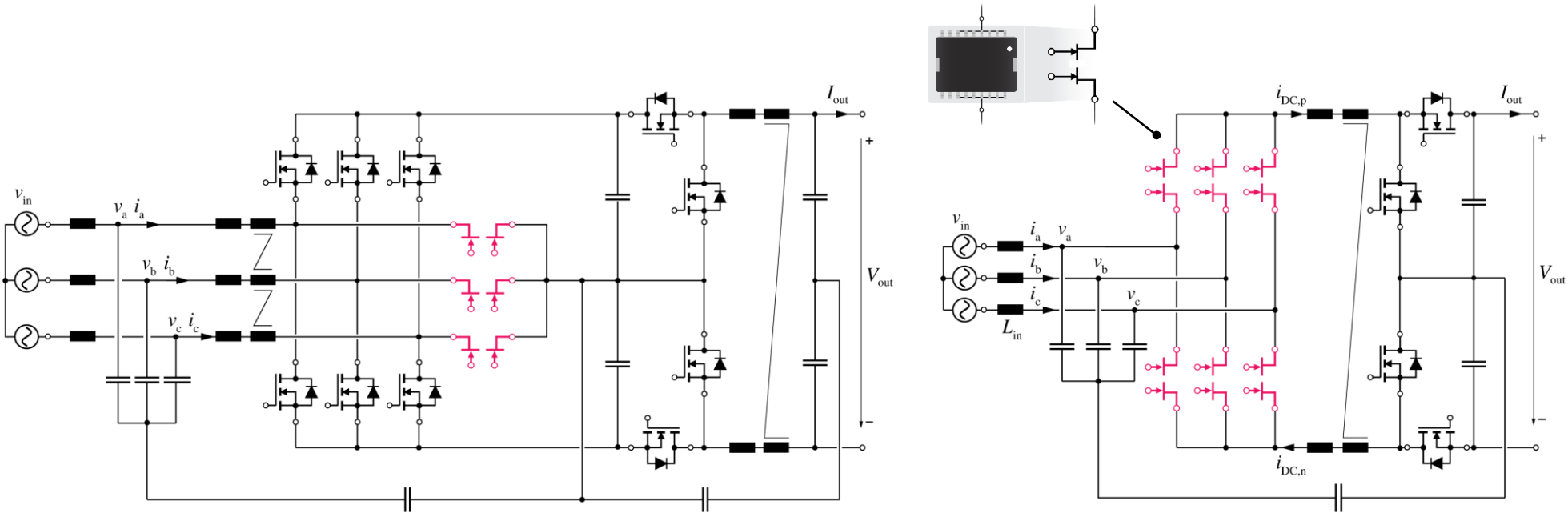
- **“Synergetic” Control of CSR-Stage & Boost-Stage → 6-Pulse i_{DC} / Significant Red. of Sw. Losses**
- **Seamless Transition from Full Boost Operation @ 6-Pulse i_{DC} to Buck Operation @ $i_{DC} = \text{const.}$**



- **“Virtual Ground” Control of Output Voltage Midpoint → Non-Isolated EV Charger (!)**

Remark Application of M-BDS

- **Boost—Buck OR Buck—Boost Combination**
- **Closed Loop vs. Open Loop Mains Current Control & Active Input Filter Damping**
- **“Synergetic Control” of AC/DC and DC/DC Converter Stage**



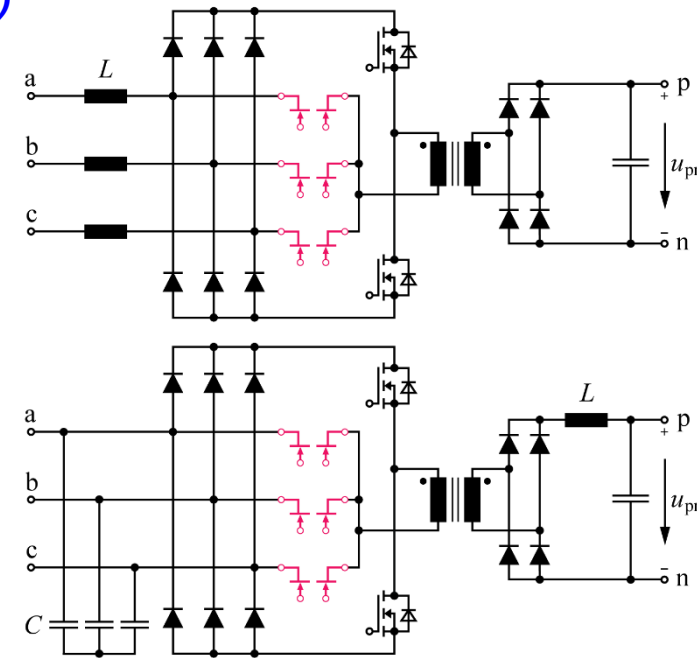
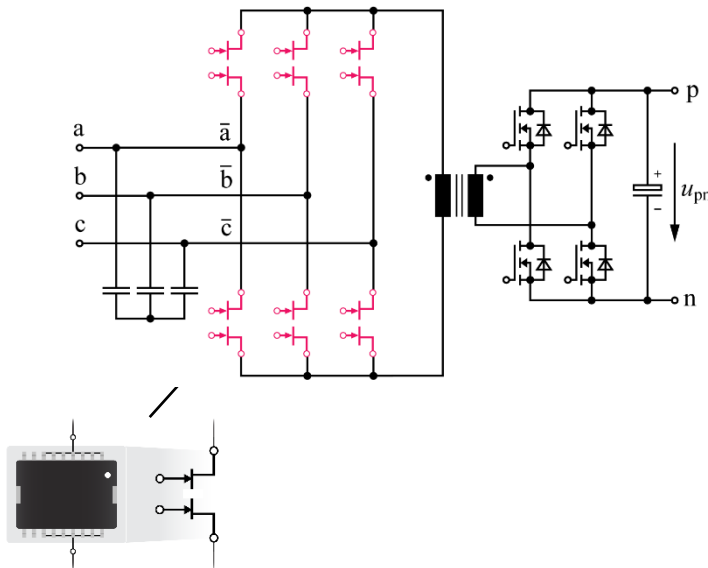
- **600 V M-BDSs for Boost—Buck & 1200 V M-BDSs for Buck—Boost Combination @ 400 V_{rms} Mains**



Outlook

Isolated Single-Stage PFC Rectifier Systems

- Matrix-Type Bidirectional DAB-Based Topology
- Vienna Rectifier II (Boost-Type / Unidirectional)
- Vienna Rectifier III (Buck-Type / Unidirectional)



- Functional Integration → Lower Complexity BUT Limited Controllability

Thank you!

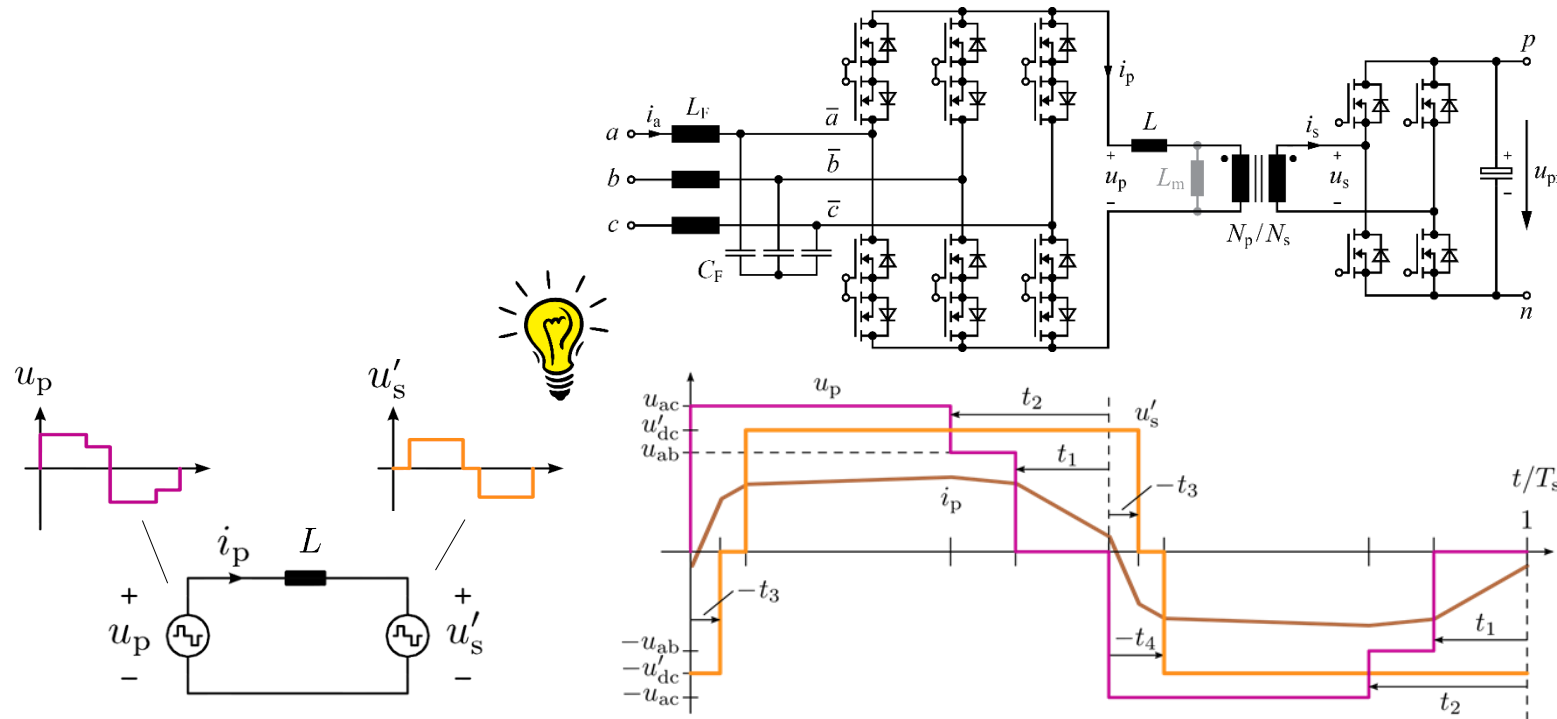




————— *3- Φ Isolated
Matrix-Type PFC
Rectifier* —————

Isolated Matrix-Type PFC Rectifier (1)

- Based on Dual Active Bridge (DAB) Concept
- Opt. Modulation ($t_1 \dots t_4$) for Min. Transformer RMS Curr. & ZVS or ZCS
- Allows Buck-Boost Operation



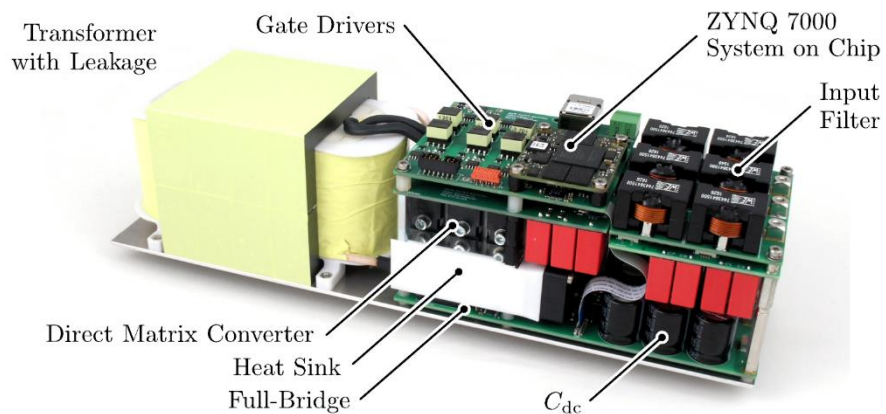
• Equivalent Circuit

• Transformer Voltages / Currents

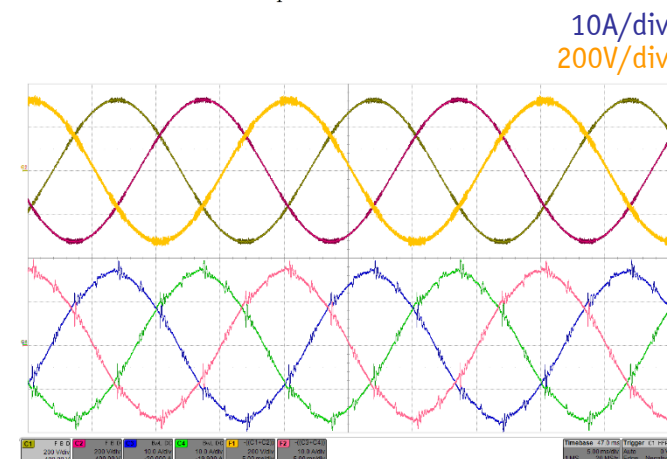
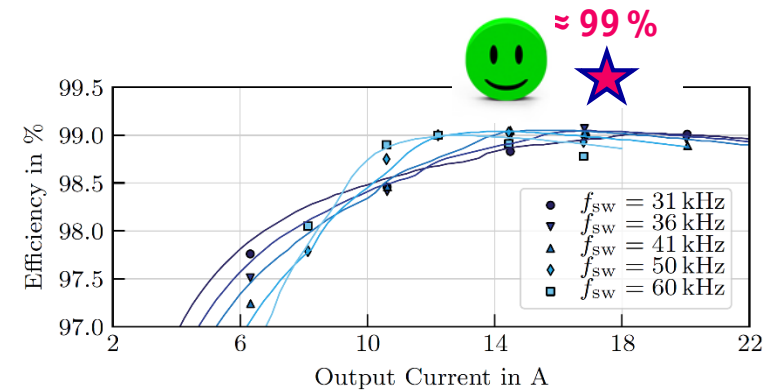
Isolated Matrix-Type PFC Rectifier (2)

- Efficiency $\eta = 98.9\%$ @ 60% Rated Load (ZVS)
- Mains Current $THD_I \approx 4\%$ @ Rated Load
- Power Density $\rho \approx 4 \text{ kW/dm}^3$

$$\begin{aligned} P_o &= 8 \text{ kW} \\ U_N &= 400 \text{ V}_{AC} \rightarrow U_o = 400 \text{ V}_{DC} \\ f_s &= 36 \text{ kHz} \end{aligned}$$



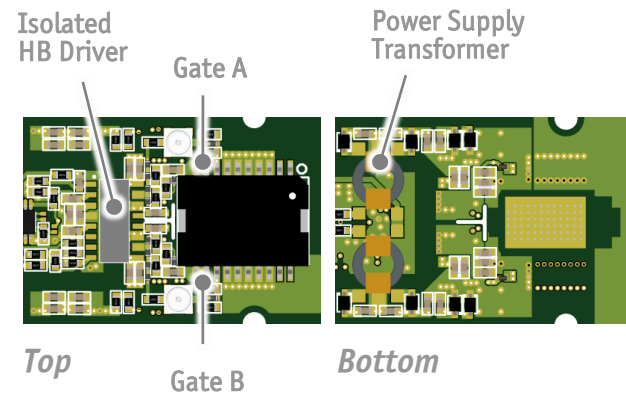
- ▶ 900 V / 10m Ω SiC Power MOSFETs
- ▶ Opt. Modulation Based on 3D Look-Up Table



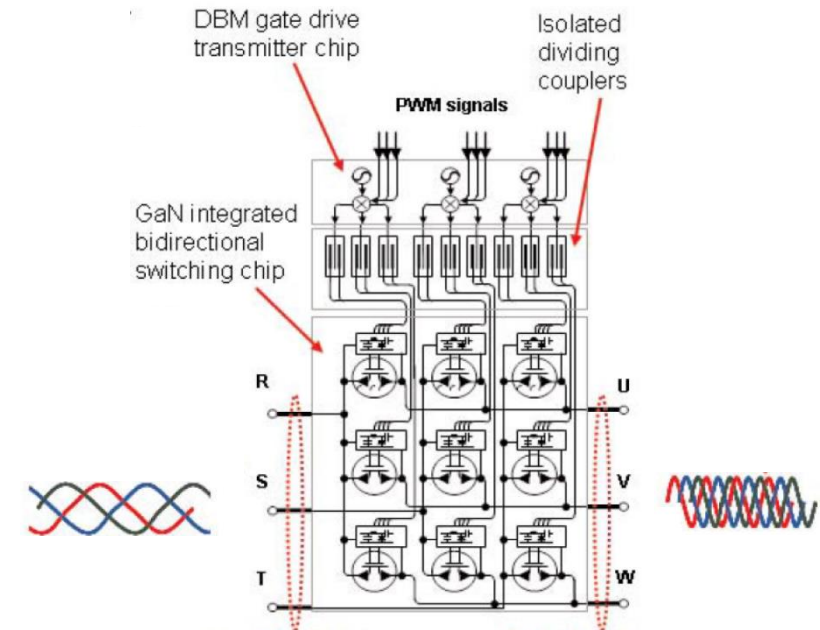
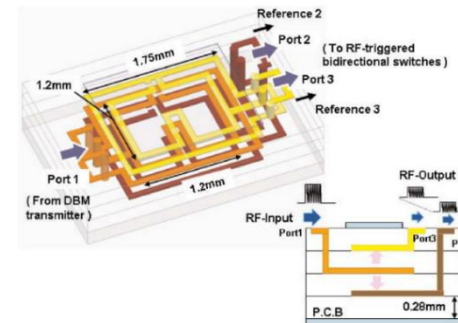
Monolithic 3D-Integration

Source: **Panasonic** ISSCC 2014

- **M-BDS GaN 3x3 Matrix Converter with Drive-By-Microwave (DBM) Technology**
 - 9 Dual-Gate GaN AC-Switches / 4-Step Commutation
 - DBM Gate Drive Transmitter Chip & Isolating Couplers
 - Ultra Compact → 25 x 18 mm² (600 V, 10 A – 5 kW Motor)



5.0GHz Isolated (5kV_{DC}) Dividing Coupler



- **Massive Space Saving Compared to Discrete Realization (!)**

 **End**